Course Title	()	() Semi d	conductor Processing for Nano Technology
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() Lecturer	()	/ / (Course No. /)	007272/ /3
(/HP) Contact No.		/ (CLass Hour/Venue)	
(Course Prerequisite)	Semiconductor nanodevices ()	(Target Student)	Students interested in semiconductor devices and process
E-mail		/Office Hour	810 , 403
(E-mail Address)		(Office/Office Hour)	
(Obj ectives)	The aim of this class is to lear for making semiconductor devices		s and integration process /
CQI (Continuous Quality Improvement Plan) (Text book & References)	Fundamentals of Semiconductor Fabrication (Gary S. May. Simon M. Sze, Wiley International Edition)		
(Assignment book)			
(Lecture Methods)	The class is to be taught based .	on the contents of the	refernce book.
(Assi gnment)			
(Reading Materials)	Semiconductor Physics and Device Author: Donald A. Neamen Publish		Third edition
가 (Course Grading)	[7t] (%): 30, Midterm Exam(30~40%), Finalte (10~20%)	(%): 40, 기 erm Exam. (40~50%), P	
(Etc.)	This class is taught in english.		

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(Week)	(Course Contents)	(Etc.)	
1	1ch. Introduction		
2	2ch. Crystal Growth		
3	3ch. Silicon Oxidation		
	3ch. Silicon Oxidation 4ch. Lithography		
5	4ch. Li thography		
6	5ch. Etching		
7	6ch. Diffusion		
8	Midterm examination		

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(Week)	(Course Contents)	(Etc.)	
	6ch. Diffusion	(210.)	
9			
	7ch. Ion Implantation		
10	7CH. TOH THIPTAILEACTOR		
	7ch. Ion Implantation 8ch. Film Deposition		
11			
	8ch. Film Deposition		
12			
13	9ch. Process Integration		
13			
	10ch. IC manufacturing		
14			
	10ch. IC manufacturing		
15	Tooli. To mandraetal Fing		
	Finalterm examination		
16			

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	Students who require special assistance (including special needs students) may contact their professors during the first week of the semester to discuss issues related to attendance, lectures, assignments and exams and request learning assistance.
	Those who would like to work in the fields like, semicoductor industry, solar cell, LED, display industry should take this class. , , LED
가 1 (Addi ti onal Gui de1)	
Gui de i)	
가 2 (Additional Guide2)	