

	1	가	3-3-0 / GRADE
	1	2	VLSI DSP 3

(Course Overview)

1. (RSA, ECC), (DES, AES), (SHA), (RC4),

2. , Verilog HDL

3. (%)

PPT

4. 가 (%)

70%	/ %	/ %	%	/ 30%	%
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30%	30%	%	10%	20%	%	%	%
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(Course Objective)

(Course Format)

(*1-3)

PPT

가 (Course Requirements and Grading Criteria)

(*4 가)

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(Course Policies)

(Materials and References)

William Stallings, Cryptography and network security, fifth edition (ISBN 0-13-6097049)

(Course Schedule)

(*)

1		ch1, ch2
2		: Data Encryption Standard ch3, ch4
3		: Advanced Encryption Standard ch5
4		: Advanced Encryption Standard ch6
5		: RC4, Pseudorandom number generation ch7
6		Verilog
7		
8		: RSA ch8, ch9
9		: ECC ch 10
10		: Secure Hash Algorithm ch 11
11		
12		
13		
14		
15		

(Special Accommodation)

: (821-5057)