

	2	가	3-3-0 / GRADE
	1	2	3

(Course Overview)

1.	Jamming Sequence Jamming					
2.						
3.	(%)					
4.	가 (%)					
	80%	/ 20%	/ %	%	/ %	%
	50%	50%	%	%	%	%

(Course Objective)

Jamming Signal
Jamming

(Course Format)

(*1-3)

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가 (Course Requirements and Grading Criteria)

(*4 가)

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(Course Policies)

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(Materials and References)

Spread Spectrum Communications Handbook

(Course Schedule)

(*)

1		Introduction
2		Pseudonoise Binary Shift Register Sequence
3		Pseudonoise Binary Shift Register Sequence
4		Pseudonoise Binary Shift Register Sequence
5		Basic Concept and System Models
6		Basic Concept and System Models
7		Basic Concept and System Models
8		Anti-Jamming Performance of FHSS System
9		Anti-Jamming Performance of FHSS System
10		Anti-Jamming Performance of FHSS System
11		Anti-Jamming Performance of FHSS System
12		Anti-Jamming Performance of DSSS System
13		Anti-Jamming Performance of DSSS System
14		Anti-Jamming Performance of DSSS System
15		Anti-Jamming Performance of DSSS System

(Special Accommodation)

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