

(SYLLABUS)

1. (*)

Item with (*) are only for the ABEEK Program.

(Year)	2016		(Semester)	1	
(Instructor)			(Course Title)	()	
(Course No.)	2150198404	(Class)	04	(Course Classification)	-
	100				
(Open to)	3	(Credit)	3 (1)	(Class hour per week)	3
(*) (ABEEK Classification)	-	(*) (ABEEK Requirement)	-	(Department)	
(Office)	051307	(Telephone)		(e-mail)	
(*) (Teaching Assistant)	e-campus	(*) (Office Hour)	10~11AM, 2~3PM		
(Course Description)	가				
(Ssq)	(Course Objectives)				
1					
2					
3					
(Lecture Type)	(90 %)	(00 %)	(00 %)	(10 %)	
가 (Course Grading)	35%,	40%,	10%,	10%,	5%
(Required Texts)	* /Microelectronic Circuits/sedra/smith/Oxford/2011/6/ * /Electronic Principles/Malvino, Bates/McGraw Hill/2006/7				
(*) (Bulletin Board)	e-campus				
(*) (Prerequisite Courses)	(2-1,),		(2-2,)		

2.

(Week)	(Keyword)	(Description)	(Texts)	(Note)
1		[1] ()	Chapter 1	
2	junction diode	[2] diode junction diode	chapter 3	

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(Week)	(Keyword)	(Description)	(Texts)	(Note)
3	PN	[3] Diode PN-junction 가 가 가	chapter 3	
4		[4] Diode	chapter 3	
5	SPICE	[5] . SPICE	chapter 3	
6	BJT, NPN transistor, PNP transistor	[6] BJT , npn pnp convention , BJT	chapter 4	
7	DC analysis	[7] BJT 가 DC	chapter 4	
8	BJT	[8] BJT	chapter 4	
9		[9] BJT BJT	chapter 4	
10	1, Ebers - Moll model, BJT inverter	[10] BJT single stage amplifier , BJT Ebers - Moll model BJT inverter BJT SPICE	chapter 4	
11	Enhancement MOSFET, depletion MOSFET	[11] MOSFET enhancement MOSFET enhancement MOSFET - , depletion MOSFET MOSFET DC	chapter 5	
12	MOSFET, biasing	[12] MOSFET MOSFET single stage IC MOS amplifier	chapter 5	

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(Week)	(Keyword)	(Description)	(Texts)	(Note)
13	CMOS inverter, JFET, MESFET	[13] MOSFET CMOS login inverter, MOSFET 가 JFET MESFET MOSFET SPICE MOSFET	chapter 5	
14	OP-AMP, inverting configuration	[14] OP amp(Operational Amplifier) 가 OP amp inverting configuration . OP amp	chapter 2	
15	, , summer, inverting configuration	[15] OP amp OP-AMP , , 가 가 summer .	chapter 2	
16	OP AMP , 가	[16] OP amp open loop gain OP amp 가	chapter 2	

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3. 가 (*)

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